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Facsimile Number: 703-872-9306

Transmission Date:

November 12, 2004

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kuzmenka, et al.

Docket No.:

INF-129

Serial No:

10/759,103

Art Unit:

2824

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Date Filed:

January 20, 2004

NOV 1 2 2004

Title:

Buffer Amplifier Architecture for Semiconductor Memory Circuits

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